

10083254
02/27/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10083254	FILING DATE 02/27/2002	CLASS 716	SUBCLASS 121	GAU 205	EXAMINER GANDHI
----------------------	---------------------------	--------------	-----------------	------------	--------------------

**APPLICANTS: Nakamoto Shigeru; Kondo Hideaki; Arima Juntarō;

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO 500.41300X00	
TITLE : Semiconductor manufacturing apparatus and its diagnosis apparatus and operating system U.S. DEPT. OF COMM. PAT. & T.M. FPO 436 (Rev. 12-94)			

BEST AVAILABLE COPY

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
TERMINAL DISCLAIMER		PREPARED FOR ISSUE		
Application Examiner				
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.				

FILED WITH: DISK (CRF) CD-ROM
(Attached in pocket on right inside flap)